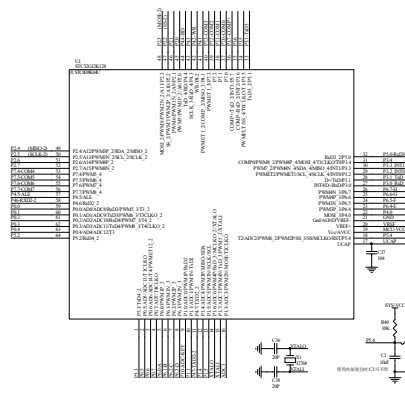
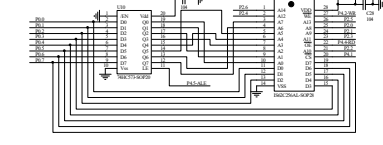


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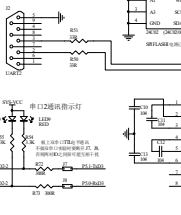
说明:
1. 本图仅供参考, 仅供参考。
2. 本图仅供参考, 仅供参考。
3. 本图仅供参考, 仅供参考。
4. 本图仅供参考, 仅供参考。
5. 本图仅供参考, 仅供参考。



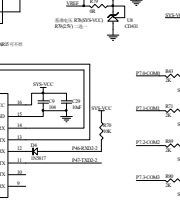
PN1为高阻 1P SRAM 处于非选中状态。
256K SRAM 处于非选中状态, 不影响单机的 I/O 口正常工作。



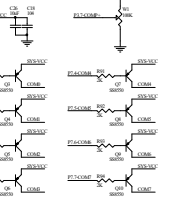
2线制 I2C 串行总线接口实验



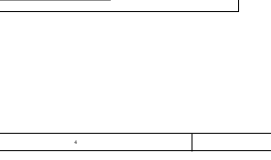
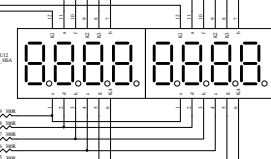
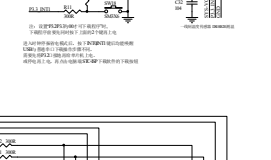
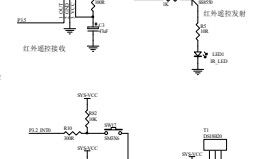
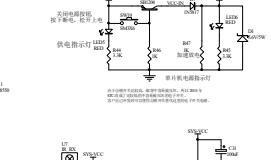
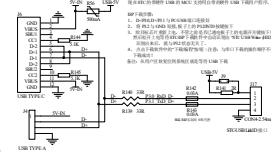
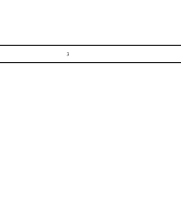
与内部 1.9V 基准电压比较, 电压比较器



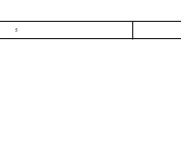
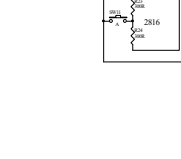
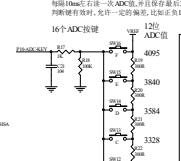
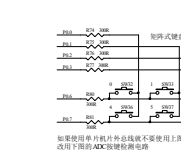
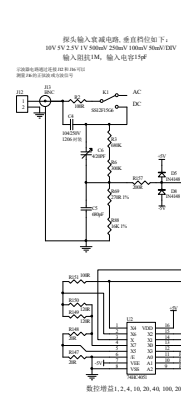
低电平驱动



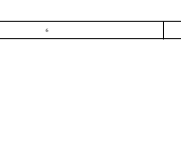
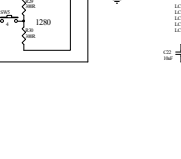
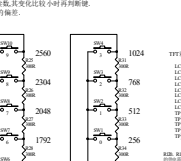
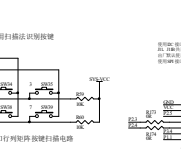
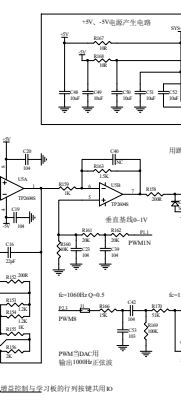
8个独立 LED 指示灯实验



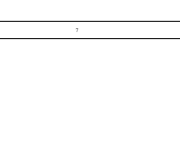
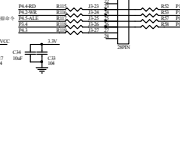
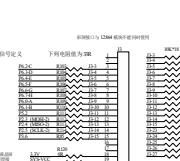
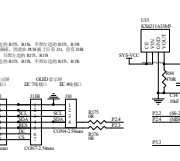
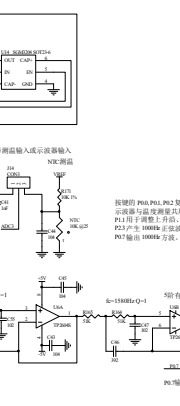
示波器输入电路、数模放大和电压电路



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